

Can adaptive capacitors improve Dr imaging?

Adaptive capacitors can be widely used in various pixel structures to achieve high DR imaging. Based on the 55 nm CMOS process platform, the research on an adaptive capacitor to improve the DR is carried out in a 12,288 × 12,288 ultra-large array infrared image sensor chip.

Can MOS capacitors be used for high-dynamic infrared image sensors?

To study the real effect of inversion MOS capacitors for high-dynamic infrared image sensors, the 55 nm 1P4M CIS process platform was used to build a 12,288 × 12,288 pixel array infrared image sensor structure based on an adaptive capacitor. The structure of the adaptive capacitor infrared image sensor is shown in Figure 3.

What is a dual-capture wide dynamic range CMOS image sensor?

Abstract: A dual-capture wide dynamic range CMOS image sensor using an in-pixel floating-diffusion (FD) storage capacitor is proposed. The proposed structure uses the FD as a storage capacitor. The potential of the FD node is read out using a floating-gate capacitor without a contact metallization of the FD node to reduce the leakage.

What is adaptive capacitance based on infrared pixel structure?

To this end, a highly dynamic pixel structure based on adaptive capacitance is proposed, so that the capacitance of the infrared image sensor can automatically change from 6.5 fF to 37.5 fF as the light intensity increases.

Does adaptive integrating capacitor improve pixel CDs performance?

It achieves excellent performance with low noise in low light. To study the change in capacitance value of the adaptive integrating capacitor under different light intensities, the pixel CDS signals using the adaptive integrating capacitor and a fixed capacitance value capacitor as the integrating capacitor is compared.

Which CMOS image sensor has two-stage lateral overflow integration capacitor?

An over 120 dB single exposure wide dynamic range CMOS image sensor with two-stage lateral overflow integration capacitor [J]. IEEE transactions on electron devices, 2021, 68 (1): 152-157.

Analysis of dynamic-range (DR) and signal-to-noise-ratio (SNR) for high delity, high-dynamic-range (HDR) image sensor architectures is presented. Four architectures are considered: (i) ...

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Capacitor dynamic image analysis

This letter proposes a novel high dynamic range (HDR) pixel using lateral overflow integration capacitor (LOFIC) and adaptive feedback structure. Through detailed ...

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CCD is a dynamic analog (charge) shift register. It consists of a series of MOS capacitors coupled with one another. CCD is clocked, and all operations are in transient mode.

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Capacitors
o A capacitor is a circuit component that consists of two conductive plates separated by an insulator (or dielectric).
o Capacitors store charge and the amount of charge stored on the ...

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This paper presents an improved model to accurately estimate the dynamic behavior of the PPD capacitance at low and high light intensity levels and varying temperature ...

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Capacitor dynamic image analysis

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